

Administrative

- I **PCI stuff due next week****
 - Group Work**
- II **Get moving with Project****
- III **Cache Check off after section****

DMA

- I **DMA - allows memory transfers w/o CPU****
- II **Specific Functions****
- III **More Genral Devices I/O Processors****

Buses and I/O

III Bus Transactions

- 1 Send Address**
- 2 Send/Receive Data**

IV Design options

- 1 Bus Width**
- 2 Data Width**
- 3 Transfer Size**
- 4 Bus Master**

Buses and I/O

IV Design options

- 5 Split Transaction**
- 6 Clocking**

V Interrupt driven I/O

VI Interrupts allow CPU to run waiting

Homework Questions

- I Question**
- II Suggestions**

Buses and I/O

- I Two Major Advantages for BUS**
 - 1 Low cost**
 - 2 Versatility**
- II Types of Buses**
 - 1 CPU-Memory Bus**
 - 2 I/O Bus**

CS 152

Discussion Section 102: Friday 12:30-2

Discussion Note #11: April 5, 1996

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Agenda

- I Homework Discussion (15 min)**
- II Buses and I/O**
- III DMA**
- IV Administrative/Break (5 min)**
- V Diagnostic Quiz Disc (15 min)**
- VI Cache Check**