

CS152
Laboratory #5: Design Review
 Due Week of March 11, 1996

CS 152

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Group Name: _____
 Group Account: _____
Total Points: _____

I	Introduction.....	(/05)
A	Cover sheet	(/01)
B	Table of Content	(/01)
C	Background.....	(/03)
II	Laboratory Exercises	(/80)
A	Step 1: Multicycle Datapath	(/20)
	1 Changes in Datapath	
	2 State registers	
	3 Branches	
B	Step 2: Multicycle Control	(/10)
	1 Brief description	
	2 Control design issues	
C	Step 3: Pipelined Datapath	(/30)
	1 Design decisions	
	2 Pipelined Datapath	
	3 Integration of new memory	
	4 Forwarding	
	5 Branches	
D	Step 4: Pipelined Control.....	(/10)
	1 Brief description	
	2 Control design issues	
	3 Changes from Multicycle control	
E	Complete Pipeline Datapath	(/10)
	1 Show that datapath is complete and functional	
III	Conclusion	(/05)
A	Results	
B	Experience - Partnership	
C	Future	
IV	Appendix.....	(/10)
A	Other Testing Vectors	
	1 On all new components	
	2 Testing Vectors for Pipeline Datapath	
B	VHDL code listings for new components	
C	Logs and others	